

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	:	Schaefer et al.	,	hereby certify that this paper is being eposited with the United States Posta		
Ser. No.	:	10/668,683	`	ervice as First Class Mail, postage repaid, in an envelope addressed to:		
Filed	:	September 23, 2003	`	1S Missing Parts		
Title	:	Circuit Device With Clock Pulse Detection Facility	) A	P.O. Box 1450 Alexandria, VA 22313-1450, on this date  Date: February 17, 2004		
Art Unit	:	2816	)			
Examiner	:	Not Yet Assigned		aul C. Craane egistration No. 38,851		

## INFORMATION DISCLOSURE STATEMENT

MS Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §1.56, the documents listed on the enclosed PTO-1449 form are disclosed to the Examiner for consideration in connection with the present application. Copies are not enclosed pursuant to the waiver of 37 C.F.R. §1.98(a)(2)(i) for patent applications filed after June 30, 2003. The listing of documents on the PTO-1449 form is not an admission that they are prior art or material to patentability as defined in 37 C.F.R. §1.56(b).

This Information Disclosure Statement is filed before the mailing date of the first Office Action on the merits, so no fee is believed to be due. However, the Commissioner is hereby authorized and directed to charge any required fee for the submission of this information disclosure statement to Deposit Account No. 13-2855. A copy of this paper is enclosed.

Respectfully submitted,

MARSHALL, GERSTEIN & BORUN LLP

Date: February 17, 2004

By:

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Examiner Initials	Document Number	Issue or Publication Date	Name	Class	Subclass	Filing Date (If Appropriat	
	2001/0043099	11/22/2001	Kawasaki et al.	327	158		
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